

SAI NEERAJ KANDOORI

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CAREER OBJECTIVE:

Electronics and Computer Engineer with a Master's Degree quest to work in a focused professional atmosphere that enables to accelerate growth for global excellence.

PROFESSIONAL SUMMARY:

- Electronic Engineer with 8 years of hand on experience in analog and digital circuit designing/layout, developing and testing hardware circuits.
- Solid understanding of electrical engineering theory, digital system, computer hardware, electronic circuit design, schematic capture, PCB layout, and testing tools
- Extensive experience on **P-Spice, Altium MATLAB, PAD's**.
- Experience working on Printed Circuit Board designing using PADS Hand on experience in soldering the electronic components
- Implementation of hardware circuit using **P-spice/OrCAD/Altium**.
- Experience on verification and testing Hardware Board design
- Designed high efficiency LC Voltage controlled Oscillators.
- Developed the 3D models and mechanical drawings using **Solid Works**.
- Experience in using the C/C++ Standard Template Library (STL) and OOP.
- Experience working with debugging tools like IAR test bench and **Visual studios**.
- Advanced level of use of engineering test and measuring equipment like multi-channel digital oscilloscopes, data acquisition systems, precision meters and various electronic related test equipment.
- Knowledge in implementing the applications under multiple OS platforms like Windows, Mac, Linux.
- Proficient in MS Excel, Word, and PPT.

PROFESSIONAL EXPERIENCE:

Thales Avionics, Bangalore /CA
Senior Electrical Engineer

Oct 2021- Present

Responsibilities:

- Designing and Developing IFE Smart Video Display Units, AC-DC power Supplies, Audio systems
- Creating component level schematics, design, and providing oversight of the development of printed circuit boards (PCBs).
- Performing systems and subsystem hardware and hardware/software integration and test.
- Debug PCB analog, digital using test equipment including oscilloscopes, logic analysis, spectrum analyzer, etc.
- Performing Design and production verification Test.
- Worked on developing the engineering documents and drawings, engineering orders, work orders etc.

Smith Medical, MN
Electronics Embedded Engineer

FEB 2020-Oct 2021

Responsibilities:

- Worked on Developing of C/C++/C++ v11 embedded software applications for both new and existing infusion pumps and other medical devices.
- Debugging the firmware using IAR workbench.
- Developing the medical devices simulation models using Visual studios
- Developing algorithm, CLI commands and conduct the design testing.
- Creating the API's and validating the Electronics Circuit Boards.
- Develop Software Design Documentation, test plans and test reports.
- Assist software platform architects and other developers and technical writers in creating software platform documentation.
- Worked with GitHub database and JIRA and Agile project management application

Amphenol Advance Sensors, CA
Electronics Design Engineer

SEP 2016 – Jan 2020

Responsibilities:

- Designing advanced electro-optical devices - prototypes of IR gas sensors.
- Designing, routing and evaluating electronic circuits, which includes high complex electronics components using PAD's routing and layouts, Mentor Graphics development tools and SPICE simulation.
- Troubleshooting and performing root cause analysis of failed engineering prototypes of electronic circuits.
- Prototypes testing and Design Validation.
- Microscopic soldering and de-soldering on multi-layer electronic circuit boards
- Component selection and creating bills of material (BOM).
- Creating Failure Modes Effect Analysis (FMEA), and Root Cause Analysis.
- Developed manufacturing flow diagrams and PFMEA and responsible for manufacturing process decisions.
- Developing technical documentation, engineering drawings and creating validation test reports.
- Creating 3D design modeling and engineering drawings using Solid Works.
- Developing test methods, electronic equipment and test procedures for executing DV/PV test plans for multiple electronic level testing.
- Analyze validation test data to ensure the data meets validation criteria.
- Advanced level of use of engineering test equipment like environmental chambers, vibration table, NI Devices, EMC (0-3 GHZ) test equipment.
- Knowledge on I2C, RS232 communication.

Skills Used: P-Spice, PAD's Layout and Logics, NI Devices, Test Chambers, Relay Boards, Embedded Work Bench, M.S.Excel, Solid works

California State University, Northridge
Graduate Teaching Assistant/Grader

May 2016-Dec 2016

Responsibilities:

- Provided training in hardware circuit designing on breadboard and simulation of designed circuits using P-Spice.
- Training to use different electronics equipment's like oscilloscope, AC/DC Power supplies, Multimeters.
- Successfully balanced student workload with teaching workload.
- Collaborated with a team of faculty at weekly meeting and actively contributed new ideas on teaching.
- Consistently received positive teacher evaluations from students.

Skills Used: P-Spice, MATLAB, oscilloscope, AC/DC Power supplies, Multimeters.

EDUCATION:

Master of Science in Electronics and Computer Engineering

California State University Northridge, CA

PROJECTS

A Physical Based Frequency Dispersion Model of GaN MESFETs

- Gallium Nitride MESEFETs is developed to determine the frequency dispersion of Output resistance and Trans conductance due to trap.
- The frequency response has been determined by evaluating the channel current, Trans conductance, gate-source capacitance and gate-drain capacitance with/without the trap center effect.
- Characteristics of the GaN MESFET'S such as the Drain current, trans conductance, gate length, and frequency for its analytical model are examined using **MATLAB**.

Bachelor of technology in Electronics and Communication Engineering

Jawaharlal Nehru Technological University, Hyderabad, India

PROJECTS

LC Oscillator using Esaki Tunnel Diode

- Designed and developed LC Oscillator using Esaki Tunnel Diode of 1GHZ frequency(1ns oscillation time period) using on P-Spice Simulation and schematic capture
- Developed working ABM (Analog Behavior models) models. of Tunnel Diode
- Characterized the behavior of LC VCO.